

Randolph E. (Randy) Harr

Extended Bio

Randy Harr runs Sevni Technology, a consulting partnership that specializes in advanced technology and research ideas getting matched to business prospects. As part of this effort, he served on the founding board of the ACM Queue magazine. This magazine quickly developed into the Scientific American for the computing industry. Randy guest edited several issues and was responsible for helping set the editorial calendar, injecting new ideas such as the PDF electronic delivery form and infamous interviewing the up and comers. He has also continued to provide consulting roles to DARPA on development of new programs, advanced technology ideas, and procurement advice.

Before hanging out his own shingle again, Randy had most recently co-founded of Intransa, an IP-based storage-networking startup. As its VP of engineering and chief architect, he took vague initial concepts and turned them into a feasible entry-level product with a solid 5 year ramp of innovations to expand the architecture. Mr. Harr co-lead raising \$17.5M in a first round of funding during the nuclear winter of funding in 2001.

Previous to this, Mr. Harr spent 5 years with the Synopsys Advanced Technology Group where he conceived, built and directed advanced product development. This included the acclaimed Nimble Compiler effort on dynamically reconfigurable computing, the EIA MCM bare die standards effort, and a reverse synthesis behavioral model compiler.

While at Synopsys, he was recognized and tapped for a 3 year appointment to the Microsystems Technology Office (MTO) of the Defense Advanced Research Projects Agency (DARPA). At DARPA, Mr. Harr focused on large-scale, embedded DSP architectures. He also led efforts on design tools for everything from large digital systems to MMIC's and MEM's devices. Before leaving, he developed an office-wide guiding vision of large-scale, mixed-technology integration – combining the office strengths of MEMS, transistors, optics and bio-fluidics into monolithic, high-reliability, true systems-on-a-chip. The transitioning focus led to a renaming of the office from Electronics Technology to Microsystems Technology and the creation of a new congressional budget line item (PE 0603739E Project MT-15) which has likely expended over \$1B to date and continues to expand in scope and size within the agency.

Prior to Synopsys, Mr. Harr had co-founded and help originate several startup concerns: C.A.D.onomist -- a computer engineering consulting, training and development services firm, Aspect Development – an electronic component procurement and management system (now part of I2), and Endot Inc. – an early (multi-)processor architecture modeling and design EDA tool (now part of Synopsys).

Mr. Harr has personally developed or been a part of many leading-edge, large scale software, hardware and HW/SW systems including on-line test ASIC's, high performance multi-processor boards, graphics display systems, laser-based sensors for sports, and a chemical (organic) modeling laboratory. He has worked on everything from the life cycle and failure testing of IC components to specification development for large, parallel processor systems. He has been involved in the development of many standards and products used in the EDA industry, helping co-develop the first commercial VHDL implementation and the VHDL language while at IBM. In 1988, as an outgrowth of his activities, Mr. Harr started the VHDL Users' Group and its online eda.org/vhdl.org resource – an international organization that grew under his leadership to 3500 members and fostered the exchange of information and technology related to VHDL. The online resource still runs strong today and the User Group is now part of Accellera.

Mr. Harr has an M.S.E. and B.S.E., Computer Engineering from Case Western Reserve University, Cleveland, OH; and University of Edinburgh, Edinburgh, Scotland. He was an IBM Yorktown Heights doctoral fellow at CWRU when he left to help startup ENDOT Inc. He is a member of the IEEE and ACM, was co-editor of an IEEE Computer special issue on configurable computing, a member of many conference program and executive committees, and has many publications and a book to his credit.